

**NONPROVISIONAL PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Attorney Docket No.: 103903

Date: July 27, 1999

**BOX PATENT APPLICATION**

**NONPROVISIONAL APPLICATION TRANSMITTAL  
RULE §1.53(b)**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Transmitted herewith for filing under 37 C.F.R. §1.53(b) is the nonprovisional patent application

For (Title): ELECTRONIC CAMERA

By (Inventors): Akira EZAWA

- ☒ Formal drawings (Figs. 1-11; 11 sheets) are attached.
- ☒ A Declaration and Power of Attorney is filed herewith.
- ☒ An assignment of the invention to Nikon Corporation is filed herewith.
- ☒ An Information Disclosure Statement is filed herewith.
- ☐ A statement to establish small entity status under 37 C.F.R. §§1.9 and 1.27 is filed herewith.
- ☐ A Preliminary Amendment is filed herewith.
- ☐ Please amend the specification by inserting before the first line the sentence --This nonprovisional application claims the benefit of U.S. Provisional Application No.         , filed         --
- ☒ Priority of foreign application No. 10-213207 filed July 28, 1998 in Japan is claimed (35 U.S.C. §119).
- ☒ A certified copy of the above corresponding foreign application is filed herewith.
- ☒ The filing fee is calculated below:

**CLAIMS IN THE APPLICATION AFTER ENTRY OF  
ANY PRELIMINARY AMENDMENT NOTED ABOVE**

FOR:	NO. FILED	NO. EXTRA
BASIC FEE		
TOTAL CLAIMS	10 - 20	= 0
INDEP CLAIMS	1 - 3	= 0
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS PRESENTED		

\* If the difference is less than zero, enter "0".

**SMALL ENTITY**

RATE	FEE
	\$ 380
x 9 =	\$
x 39 =	\$
+130 =	\$
<b>TOTAL</b>	<b>\$</b>

**OTHER THAN A  
SMALL ENTITY**

RATE	FEE
	\$ 760
x 18	\$
x 78	\$
+260	\$
<b>TOTAL</b>	<b>\$ 760</b>

- ☒ Check No. 101913 in the amount of \$760 to cover the filing fee is attached. Except as otherwise noted herein, the Commissioner is hereby authorized to charge any other fees that may be required to complete this filing, or to credit any overpayment, to Deposit Account No. 15-0461. Two duplicate copies of this sheet are attached.
- ☐ This application is entitled to small entity status. DO NOT charge large entity fees to our Deposit Account.

Respectfully submitted,

*William P. Berridge*  
James A. Oliff  
Registration No. 27,075

William P. Berridge  
Registration No. 30,024

JAO:WPB/dlm

SECRET

**Inventor Information**

Inventor One Given Name:: Akira  
Family Name:: EZAWA  
Name Suffix::  
City of Residence:: Kawasaki-shi  
State or Prov. of Residence::  
Country of Residence:: JAPAN  
Inventor Two Given Name::  
Family Name::  
Name Suffix::  
City of Residence::  
State or Prov. of Residence::  
Country of Residence::  
Inventor Three Given Name::  
Family Name::  
Name Suffix::  
City of Residence::  
State or Prov. of Residence::  
Country of Residence::  
Inventor Four Given Name::  
Family Name::  
Name Suffix::  
City of Residence::  
State or Prov. of Residence::  
Country of Residence::  
Inventor Five Given Name ::  
Family Name::  
Name Suffix::  
City of Residence::  
State or Prov. of Residence::  
Country of Residence::

**Correspondence Information**

Name Line One:: Oliff & Berridge PLC  
Address Line One:: P.O. Box 19928  
City:: Alexandria  
State or Province:: VA  
Postal or Zip Code:: 22320  
Telephone:: (703) 836-6400  
Fax:: (703) 836-2787  
Electronic Mail:: commcenter@oliff.com

**Application Information**

Title Line One:: ELECTRONIC CAMERA  
Title Line Two::  
Title Line Three::  
Title Line Four :

Total Drawing Sheets:: 11  
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### **Continuity Information**

>This application is a::  
Application One::  
Filing Date::  
Patent Number::  
which is a::  
>>Application Two::  
Filing Date::  
Patent Number::

### **Prior Foreign Applications**

Foreign Application One:: 10-213207  
Filing Date:: July 28, 1998  
Country:: JAPAN  
Priority Claimed:: yes  
Foreign Application Two::  
Filing Date::  
Country::  
Priority Claimed::  
Foreign Application Three::  
Filing Date::  
Country::  
Priority Claimed::

# ELECTRONIC CAMERA

## INCORPORATION BY REFERENCE

The disclosure of the following priority  
5 application is herein incorporated by reference:

Japanese Patent Application No. 10-213207 filed  
July 28, 1998.

## BACKGROUND OF THE INVENTION

### 10 1. Field of the Invention

The present invention relates to an electronic  
camera. To be more specific, it relates to an electronic  
camera which is capable of photographing a higher number  
of frames per unit time (hereafter referred to as the  
15 "frame speed").

### 2. Description of the Related Art

There are single lens reflex type electronic  
cameras in the known art that separate the light flux  
from the photographing optical system to a viewfinder-  
20 side light flux and an image-capturing element-side  
light flux via a quick-return mirror or the like. FIG.  
11 shows the sequence of operations performed in this  
type of electronic camera. In FIG. 11, when the shutter  
release switch at the electronic camera is fully  
25 pressed, the internal sequence motor starts to rotate in

a state in which power is supplied to the front curtain magnet and the rear curtain magnet of the shutter.

This rotation of the sequence motor causes the quick-return mirror to swing upward and the mechanical retention of the shutter to be released. At this point, the front curtain and the rear curtain at the shutter are held in a retained state respectively by the front curtain magnet and the rear curtain magnet mentioned earlier.

When the sequence motor has rotated by a specific amount and the operation described above is completed, the sequence switch shifts to an ON state and the sequence motor is stopped temporarily. In synchronization with the shift of the sequence switch to the ON state, the image-capturing element discharges unnecessary electrical charge on the photosensitive surface and starts electrical charge storage. However, since the shutter is closed at this point, no substantial signal electrical charge storage is achieved.

After rebounding to some extent at the top of its upswing, the quick-return mirror stops. After allowing a specific length of time ( $t_{31}$  in the figure) to elapse to allow the rebounding to settle, power supply to the front curtain magnet is cut off and the front curtain at

the shutter starts its run. When a length of time corresponding to a value set for the exposure time has elapsed after the front curtain starts its run, power supply to the rear curtain magnet is cut off and the rear curtain at the shutter starts its run.

With the two curtains caused to run one after the other in this manner, the photosensitive surface of the image-capturing element is exposed over the specific length of exposure time. When the rear curtain completes its run, the rear curtain closing switch shifts to an ON state. At the image-capturing element, a read of the signal electrical charge starts in synchronization with this shift of the rear curtain closing switch to the ON state.

After the read operation of the signal electrical charge is completed, the sequence motor starts to rotate again. This rotation of the sequence motor allows the mechanical operations (mirror down, shutter charge) to be executed in preparations for photographing of the next frame. When the mirror moves down, the path through which the subject light advances is changed, and the subject light is guided to the focal point detection block and the photometric unit of the electronic camera.

As the path of the subject light is changed in this manner, the focal point detection block and the

photometric unit start measuring operations (focal point detection operation, photometry operation) in preparations for photographing of the next frame.

By repeating the sequence of operations while the shutter release switch is fully pressed down, continuous shooting is performed. The following details are known in the prior art as technologies that achieve higher frame speed in such continuous shooting.

(1) Japanese Laid-Open Patent Publication No. H 6-54252 discloses a technology that achieves an increase in the frame speed by exposing a succeeding frame at the image-capturing element while the image in the preceding frame is being compressed.

(2) In addition, Japanese Laid-Open Patent Publication No. H 7-135589 discloses a technology that achieves a higher frame speed by recording image data of an image that has been captured separately in a plurality of recording media to reduce the length of time required for image recording.

Now, higher resolution at the image-capturing element has become a vital requirement in an electronic camera in recent years to ensure that the captured image is of the highest possible quality. Higher resolution at the image-capturing element has caused the tendency of increases in the length of time required for reading the

electrical charges from the image-capturing element  
(hereafter referred to as the "electrical charge read  
period").

The increase in the electrical charge read period  
5 has caused the length of time required for photographing  
a single frame to increase correspondingly, which  
presents a problem in that the frame speed of the  
electronic camera becomes lower. It is to be noted that  
since the prior art technologies for achieving higher  
10 speed (Japanese Laid-Open Patent Publication No. H6-  
54252, Japanese Laid-Open Patent Publication No. H7-  
135589) do not specifically deal with the longer  
electrical charge read period, it has not been possible  
to solve the problem of the lower frame speed occurring  
15 for the reason described above.

#### SUMMARY OF THE INVENTION

An object of the present invention is to provide an  
electronic camera with which the problem of lowered  
20 frame speed resulting from longer electrical charge read  
period can be alleviated. Another object of the present  
invention is to provide an electronic camera that is  
capable of maintaining accurate and correct operation  
timing even when a higher frame speed is achieved.

25 In order to attain the above objects, an electronic



camera comprises: an image-capturing device that stores  
a signal electrical charge achieved by performing  
photoelectric conversion on a subject image projected on  
a photosensitive surface and reads out the signal  
5 electrical charge to generate an image signal; and a  
photographing preparation device that executes  
photographing preparations for photoelectric conversion  
performed at the image-capturing device. And, the  
photographic preparation device executes at least some  
10 of the photographing preparations for photographing a  
next frame during an electrical charge read period at  
the image-capturing device.

Preferably, this electronic camera further  
comprises: at least one of a shutter mechanism, a mirror  
15 mechanism, an aperture mechanism, a focal adjustment  
device and a photometric device. And the photographing  
preparations for the next frame include at least one of  
a shutter charge performed by the shutter mechanism, a  
mirror down performed by the mirror mechanism, a mirror  
20 up performed by the mirror mechanism, aperture control  
performed by the aperture mechanism, focal adjustment  
performed by the focal adjustment device and photometry  
performed by the photometric device, to enable  
photographing of the next frame.

25 Preferably, the electronic camera further

comprises: a shutter mechanism, a mirror mechanism and  
an aperture mechanism. And the photographing  
preparation device completes a shutter charge performed  
by the shutter mechanism and a mirror down performed by  
5 the mirror mechanism to enable photographing of the next  
frame and starts a mirror up performed by the mirror  
mechanism and aperture control performed by the aperture  
mechanism to enable photographing of the next frame  
during an electrical charge read period at the image-  
10 capturing device.

Preferably, the electronic camera further  
comprises: a mirror mechanism, a focal adjustment device  
and a photometric device. And the photographing  
preparation device completes a mirror down performed by  
15 the mirror mechanism to enable photographing of the next  
frame and then implements focal adjustment by the focal  
adjustment device and photometry by the photometric  
device, during an electrical charge read period at the  
image-capturing device.

20 Preferably, the electronic camera further  
comprises: a continuous shooting command device that  
issues a command to perform continuous shooting. And,  
the photographic preparation device executes at least  
some of the photographing preparations for the next  
25 frame during the electrical charge read period at the

image-capturing device while a command to perform continuous shooting issued by the continuous shooting command device is in effect.

Preferably, in the electronic camera, the  
5 photographing preparation device includes a drive motor that drives the photographing preparation and implements rotational drive of the drive motor during the electrical charge read period at the image-capturing device.

10 Preferably, in the electronic camera, the photographing preparation device includes a drive motor that sequentially drives a plurality of photographing preparations in correspondence to a rotating angle and implements rotational drive of the drive motor during  
15 the electrical charge read period at the image-capturing device.

Preferably, in the electronic camera, operation timing is set in advance at least at either the photographing preparation device or the image-capturing  
20 device to ensure that the electrical charge read period does not overlap a period over which a subject image of the next frame is projected onto the photosensitive surface.

Preferably, in the electronic camera, the  
25 photographing preparation device performs detection of a

completion of a signal electrical charge read operation performed by the image-capturing device and following the detection, projects a subject image onto the photosensitive surface.

5            Preferably, the electronic camera further comprises: a time count device that measures at least a part of a length of time required for the photographing preparation. And at least either the photographing preparation device or the image-capturing device adjusts  
10 operation timing to ensure that a signal electrical charge read period does not overlap a period over which a subject image for the next frame is projected onto the photosensitive surface based upon results of count of the length of required time performed by the time count  
15 device.

#### BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 illustrates the positional relationships in the internal structure of the electronic camera 11;

20            FIG. 2 is a block diagram illustrating the electrical system in the electronic camera 11;

FIG. 3 is a timing chart of a first embodiment;

FIG. 4 is a flowchart of the operations performed by the microprocessor 20 in the first embodiment;

25            FIG. 5 is a flowchart of the operations performed

by the microprocessor 20 in a second embodiment;

FIG. 6 is a block diagram illustrating the electrical system in the electronic camera 50 in a third embodiment;

5        FIG. 7 is a timing chart of the third embodiment;

FIG. 8 is a flowchart of the operations performed by the microprocessor 20 in the third embodiment;

FIG. 9 is a flowchart of the operations performed by the microprocessor 20 in a fourth embodiment; and

10        FIG. 10 is a timing chart of the focal point detection operation and the photometric operation started during the electrical charge read period.

FIG. 11 shows the sequence of operations during continuous shooting in the prior art electronic camera.

15

#### DESCRIPTION OF THE PREFERRED EMBODIMENTS

The following is an explanation of the preferred embodiments of the present invention, given in reference to the drawings.

20        (First Embodiment)

FIG. 1 illustrates the positioning of the main parts inside the camera in the first embodiment. In FIG. 1, a photographing optical system 12 is mounted at the body of an electronic camera 11. An aperture drive  
25        mechanism 13 and a quick-return mirror 14 are positioned

in this order at the photographing optical system 12 toward the image space.

A viewfinder optical system is provided along the direction in which the quick-return mirror 14 reflects light. A photometric unit 15 that takes in subject light is provided at a portion of the viewfinder optical system. In addition, a sub-mirror 16 is provided at the rear surface of the quick-return mirror 14 in its central transmission area. A focal point detection unit 17 is provided along the direction in which the sub-mirror 16 reflects light. Also, an image-capturing element 19 is provided to the rear of the quick-return mirror 14 via a shutter mechanism 18 which is provided in between.

FIG. 2 is a block diagram illustrating the electrical system in the electronic camera 11 described above. In FIG. 2, a microprocessor 20 that engages in the control of the entire system is provided inside the electronic camera 11. The microprocessor 20 controls an AF motor 22 via a motor drive circuit 21. The AF motor 22 drives the optical system back and forth for focus control provided inside the photographing optical system 12 and executes focal adjustment through the contrast method whereby the focal adjustment is assumed to be achieved when the contrast is at the highest.

In addition, the microprocessor 20 provides a drive pulse for electrical charge read to the image-capturing element 19 via a drive pulse generating circuit (not shown). An image signal output by the image-capturing element 19 is processed at an image-capturing circuit 23 which performs color signal processing and A/D conversion and then is input to the microprocessor 20 as image data. Furthermore, the microprocessor 20 implements control on a sequence motor 25 via a motor drive circuit 24. Depending upon the rotating angle of the sequence motor 25, the aperture drive mechanism 13, a mirror drive mechanism 26 and a charge mechanism 27 are driven in a specific order.

The mirror drive mechanism 26 raises and lowers the quick-return mirror 14. The charge mechanism 27 performs a sequence of operations, that is shutter charge, through which a front curtain 28 and a rear curtain 29 at the shutter mechanism 18 are reset to their pre-run positions and are mechanically retained. The front curtain 28 and the rear curtain 29 are respectively provided with a front curtain magnet 30 and a rear curtain magnet 31 that achieve curtain retention through magnetic force. The microprocessor 20 controls the electrical current supplied to the magnets 30 and 31 with a front curtain drive circuit 32 and a rear curtain

drive circuit 33.

Furthermore, a PC interface 34 is provided inside the electronic camera 11. The microprocessor 20 exchanges data, instructions and the like with an  
5 external device 35 via the PC interface 34. In addition, two timers A and B for counting time are provided inside the electronic camera 11. By initializing the count values at the timers A and B, the microprocessor 20 can measure the lengths of time elapsed after the  
10 initialization.

Moreover, the microprocessor 20 obtains photometric data indicating the subject brightness via the photometric unit 15. By engaging in exposure calculation based upon the photometric data and the photosensitivity  
15 of the image-capturing element, the microprocessor 20 determines the correct aperture value. Also, a connector 38 is provided at the electronic camera 11. Image files and the like are transferred to and recorded in a memory card 39 by the microprocessor 20 via the connector 38.

20 In addition, an image memory 40 that is provided to temporarily record image data, a compression/expansion circuit 41 that compresses and expands the image data, a memory 42 that stores a control program and data, and the like are connected to the microprocessor 20. Also, a  
25 switch group that includes a power switch 45, a shutter



release switch 46, a rear curtain closing switch 47 and a sequence switch 48, is connected to the microprocessor 20.

(Operation of the First Embodiment)

5           FIG. 3 is a timing chart of the first embodiment. In addition, FIG. 4 is a flowchart of the operations performed by the microprocessor 20. The following is an explanation of the operation achieved in the first embodiment given in reference to FIGS. 3 and 4.

10           First, a microprocessor 20 makes a decision as to whether or not the shutter release switch 46 is in a fully pressed state (FIG. 4, S1). It is to be noted that the shutter release switch 46 is a switch that may be pressed halfway down or fully down. Before the routine  
15 in FIG. 4 is started, automatic focal adjustment (AF) and photometry are performed while the shutter release switch 46 is pressed halfway down.

When the shutter release switch 46 is pressed fully down, the microprocessor 20 first starts power supply to  
20 the front curtain magnet 30 and the rear curtain magnet 31 and then starts rotation of the sequence motor 25 (FIG. 4, S2).

This rotation of the sequence motor 25 causes the mirror drive mechanism 26 to raise the quick-return  
25 mirror 14. In addition, the aperture drive mechanism 13

implements aperture control in correspondence to the correct aperture value. At the same time, the charge mechanism 27 releases the mechanical retention of the shutter mechanism 18.

5           When these operations effected by the sequence motor 25 are completed, the sequence switch 48 shifts to an ON state. After verifying that the sequence switch 48 is now in an ON state (FIG. 4, S3), the microprocessor 20 temporarily stops the sequence motor 25. At this  
10 point, the microprocessor 20 discharges any unnecessary electrical charge from the image-capturing element 19 to start an electrical charge storage period. At the same time, the microprocessor 20 initializes the count value at the timer A and starts time count (FIG. 4, S4).

15           In this state, the microprocessor 20 waits for a length of time t11 to elapse by monitoring the count value at the timer A (FIG. 4, S5). When the length of time t11 elapses, the microprocessor 20 decides that the rebounding of the quick-return mirror 14 has subsided to  
20 a sufficient degree and cuts off the power supply to the front curtain magnet 30. In response, the front curtain 28 starts its run due to the force applied to it and, as a result, the shutter mechanism 18 starts to open. At this point, the microprocessor 20 initializes the count  
25 value at the timer A to restart time count (FIG. 4, S6).

The microprocessor 20 waits for the exposure time to elapse by monitoring the count value at the timer A (FIG. 4, S7). When the exposure time elapses, the microprocessor 20 cuts off the power supply to the rear curtain magnet 31, as well (FIG. 4, S8). In response, the rear curtain 29 starts its run following the front curtain 28.

When the rear curtain 29 completes its run and the shutter mechanism 18 becomes completely closed, the rear curtain closing switch 47 switches to an ON state. When the microprocessor 20 verifies that the rear curtain closing switch 47 has shifted to an ON state (FIG. 4, S9), it supplies a drive pulse for electrical charge read to the image-capturing element 19 to start a read of the signal electrical charge. At this point, the microprocessor 20 initializes the count values at the timers A and B and starts time count (FIG. 4, S10).

In this state, the microprocessor 20 waits for a length of time t12 to elapse by monitoring the count value at the timer A (FIG. 4, S11). When the length of time t12 elapses, the microprocessor 20 restarts the rotation of the sequence motor 25 (FIG. 4, S12).

The "electrical charge read start" and the "restart of the rotation of the sequence motor 25" are shifted from each other by the length of time t12 in this

manner. As a result, the source voltage is prevented from suddenly dropping and it becomes possible to reduce the noise interference between the two operations with great reliability.

5           As the sequence motor 25 restarts its rotation in this manner, the mirror drive mechanism 26 lowers the quick-return mirror 14. In addition, the charge mechanism 27 resets the front curtain 28 and the rear curtain 29 to their pre-run positions and mechanically  
10           retains them (shutter charge operation). In addition, in order to improve the degree of accuracy of the count in the photometric operation and the focal point detection operation, the aperture drive mechanism 13 may be temporarily reset to the open aperture in response to  
15           the restart of rotation of the sequence motor 25.

          When these operations effected by the sequence motor 25 are completed, the sequence switch 48 shifts to an ON state. After verifying that the sequence switch 48 has shifted to an ON state (FIG. 4, S13), the  
20           microprocessor 20 temporarily stops the sequence motor 25 (FIG. 4, S14).

          In this state, the microprocessor 20 waits for a length of time  $t_{f1}$  to elapse on the timer B (FIG. 4, S15). This length of time  $t_{f1}$  represents the length of  
25           wait time set by anticipating a margin ( $t_{r1}$  in FIG. 3)

in advance in order to end the electrical charge read operation currently in progress before the exposure of the next frame starts with great reliability. It is to be noted that as explained later, it is mechanically assured that even when the control returns to step S2 to restart rotation of the sequence motor 25 and the like, the exposure of the next frame is not started for at least the length of time  $ts1$ . Consequently, a length of time that is shorter than the read period  $ty1$  by the length of time  $ts1$  can be set for the wait time  $tf1$ .

When the length of time  $tf1$  elapses, the microprocessor 20 makes a decision as to whether or not the shutter release switch 46 is in a fully pressed state (FIG. 4, S16). At this point, if the fully pressed state has already been cleared, the microprocessor 20 ends the continuous shooting operation. If, on the other hand, the shutter release switch is still in the fully pressed state, the microprocessor 20 returns to the operation in step S2 to continue with the sequence of continuous shooting operations described above.

In the first embodiment, the descent of the quick-return mirror 13 and the shutter charge are completed during the electrical charge read period, and then the quick-return mirror 14 is raised and the aperture control is started.

(Advantages of the First Embodiment)

As explained above, in the first embodiment, the preparations for photographing the next frame (mirror down, shutter charge, mirror up, aperture control) are started during the electrical charge read period and the preparations for photographing the next frame (mirror down, shutter charge, mirror up, aperture control) are performed concurrently with the electrical charge read. By achieving such parallel operations, it is possible to reduce the length of time required for photographing a single frame in an effective manner and a higher frame speed can be achieved with great reliability and with ease.

In addition, since the shutter is mechanically closed by allowing the wait time  $t_{f1}$  to elapse during the operation, the subject image in the next frame is not projected onto the photosensitive surface during the electrical charge read period. Thus, the smear phenomenon resulting from the effect of the light at the photosensitive surface during the electrical charge read, i.e., during the electrical charge transfer, does not occur, so that the signal electrical charge can be read out with an even higher quality result.

In the case of the technologies for achieving higher speed in the prior art, it is difficult to

achieve a higher frame speed if the electrical charge read period is lengthened as a result of assuring a higher resolution at the means for image-capturing.

However, the first embodiment, in which the

5   photographing preparations for the next frame are started in advance during the electrical charge read period and the preparations are made concurrently with the electrical charge, achieves a higher frame speed with great reliability.

10   (Second Embodiment)

Next, the second embodiment is explained. It is to be noted that since the structural features of the second embodiment are identical to those in the first embodiment except that the program operation at the  
15   microprocessor 20 is partially different, an explanation of the structural features is omitted here.

FIG. 5 is a flowchart of the operations performed by the microprocessor 20 in the second embodiment. The operation achieved in the second embodiment is  
20   characterized in that a front curtain run is started after verifying that the electrical charge read period has elapsed, as indicated in S5a in FIG. 5.

In the second embodiment, in which operational verification is performed in this manner, no problems  
25   such as the subject image in the next frame being

projected onto the photosensitive surface during the electrical charge read period occur, even if the operation timing changes due to fluctuations in the source or the like. Furthermore, since it is not  
5 necessary to set a great length of time for the wait time  $t_{f1}$  in anticipation of fluctuations in the operation timing, the frame speed can be increased to the maximum possible.

(Third Embodiment)

10 Next, the third embodiment is explained. FIG. 6 is a block diagram illustrating the electrical system of an electronic camera 50 in the third embodiment. The structural features of the third embodiment are as described below.

15 A shutter mechanism 51 of the electronic camera 50 is provided with a single curtain 52. A curtain magnet 53 that retains the curtain 52 in an open state is controlled by the microprocessor 20 via a curtain drive circuit 54. In addition, a curtain closing switch 56  
20 whose switch position state changes in correspondence to the open/closed state of the curtain 52. It is to be noted that since other structural features are identical to those in the first embodiment (FIG. 2), the same reference numbers are assigned to the relevant  
25 components in FIG. 6 to preclude the necessity for a



repeated explanation thereof.

FIG. 7 is a timing chart of the third embodiment. In addition, FIG. 8 is a flowchart of the operations performed by the microprocessor 20. The following is an  
5 explanation of the operation achieved in the third embodiment, given in reference to FIGS. 7 and 8.

First, the microprocessor 20 makes a decision as to whether or not the shutter release switch 46 is in a fully pressed state (FIG. 8, S1). When the shutter  
10 release switch 46 is pressed fully down, the microprocessor 20 first starts power supply to the curtain magnet 53 and then starts rotation of the sequence motor 25 (FIG. 8, S2). It is to be noted that in this state, the curtain 52 sustains the closed state  
15 because of the mechanical retention effected by the charge mechanism 55.

This rotation of the sequence motor 25 causes the mirror drive mechanism 26 to raise the quick-return mirror 14. In addition, the aperture drive mechanism 13  
20 implements aperture control in correspondence to the correct aperture value. At the same time, the charge mechanism 55 releases the mechanical retention of the shutter mechanism 51 to reset the curtain 52 in an open state (shutter charge operation). As a result, the  
25 curtain 52 is retained by the curtain magnet 53 to which

power is being supplied and the shutter mechanism 51 is held in an open state.

When these operations effected by the sequence motor 25 are completed, the sequence switch 48 shifts to an ON state. After verifying that the sequence switch 48 is now in an ON state (FIG. 8, S3), the microprocessor 20 temporarily stops the sequence motor 25. At this point, the microprocessor 20 initializes the count value at the timer A and starts time count (FIG. 8, S4).

The microprocessor 20 waits for a length of time t21 to elapse by monitoring the count value at the timer A (FIG. 8, S5). When the length of time t21 elapses, the microprocessor 20 decides that the rebounding of the quick-return mirror 14 has subsided to a sufficient degree and starts an electrical charge storage period by forcibly discharging any unnecessary electrical charge from the image-capturing element 19. At this point, the microprocessor 20 initializes the count value at the timer A to restart time count (FIG. 8, S6).

The microprocessor 20 waits for the exposure time to elapse by monitoring the count value at the timer A (FIG. 8, S7). During this process, when the exposure time elapses, the microprocessor 20 ends the electrical charge storage period by transferring the signal electrical charges stored at the photosensitive surface

in a batch onto the transfer line. In addition, the microprocessor 20 cuts off the power supply to the curtain magnet 53 to cause the curtain 52 to run in the closing direction by the force applied to it (FIG. 8, S8).

When the curtain 52 completes its run and the shutter mechanism 51 becomes completely closed, the curtain closing switch 56 switches to an ON state. When the microprocessor 20 verifies that the curtain closing switch 56 has shifted to an ON state (FIG. 8, S9), it supplies a drive pulse for electrical charge read to the image-capturing element 19 to start a read of the signal electrical charge. In addition, the microprocessor 20 restarts the rotation of the sequence motor 25 at this point and then initializes the count value at the timer A to start time count (FIG. 8, S10).

As the sequence motor 25 restarts its rotation in this manner, the mirror drive mechanism 26 lowers the quick-return mirror 14. It is to be noted that the aperture drive mechanism 13 may reset the aperture to an open state during this period. When these operations effected by the sequence motor 25 are completed, the sequence switch 48 shifts to an ON state. After verifying that the sequence switch 48 has shifted to an ON state (FIG. 8, S11), the microprocessor 20

temporarily stops the sequence motor 25 (FIG. 8, S12).

In this state, the microprocessor 20 waits for a length of time tf2 to elapse by monitoring the count value at the timer A (FIG. 8, S13). This length of time tf2 represents the length of wait time set in advance in order to end the electrical charge read operation currently in progress before the exposure of the next frame starts with great reliability.

When the length of time tf2 elapses, the microprocessor 20 makes a decision as to whether or not the shutter release switch 46 is in a fully pressed state (FIG. 8, S14). At this point, if the fully pressed state has already been cleared, the microprocessor 20 ends the continuous shooting operation. If, on the other hand, the shutter release switch is still in the fully pressed state, the microprocessor 20 returns to the operation in step S2 to sustain the continuous shooting operation.

(Advantages of the Third Embodiment)

As explained above, in the third embodiment, too, the preparations for photographing the next frame (mirror down, shutter charge, mirror up, aperture control) are performed during concurrently the electrical charge read period. Thus, it is possible to reduce the length of time required for photographing a

single frame correspondingly and a higher frame speed can be achieved with great reliability and with ease.

In addition, since the wait time  $t_{f2}$  is allowed to elapse during the operation, the subject image in the next frame is not projected onto the photosensitive surface during the electrical charge read period. Thus, the smear phenomenon resulting from the effect of the light at the photosensitive surface during the electrical charge read, i.e., during the electrical charge transfer, does not occur, so that the signal electrical charge can be read out with even higher quality results.

Furthermore, in the third embodiment, the shutter mechanism 51 is constituted of a single curtain to partially incorporate the electronic shutter operation. Thus, it is possible to do without the length of time required for opening and closing the front curtain 28 in the first embodiment to achieve a corresponding reduction in the length of time required to photograph a single frame. Consequently, an even higher frame speed is achieved.

(Fourth Embodiment)

Next, the fourth embodiment is explained. It is to be noted that since the structure and the timing chart of the fourth embodiment are identical to those of the

third embodiment (FIGS. 6 and 7), their explanation is omitted here.

FIG. 9 is a flowchart of the operations performed by the microprocessor 20 in the fourth embodiment. The operation achieved in the fourth embodiment is characterized as follows.

- (1) As indicated in FIG. 9, S2, the microprocessor 20 starts the timer A at the point in time at which the rotation of the sequence motor 25 starts.
- 10 (2) In this state, the microprocessor 20 monitors the switching state of the curtain closing switch (FIG. 9, S3).
- (3) The microprocessor 20 stores the count value at the timer A as a variable  $t_s$  when the curtain closing switch 15 56 shifts to an OFF state. This length of time  $t_s$  represents the length of time elapsing between the start of rotation of the sequence motor 25 and the completion of the shutter charge operation performed by the charge mechanism 55. Based upon the length of time  $t_s$ ,
- 20  $t_{f2} = t_{y2} - (0.8 \cdot t_s)$  is calculated to determine the wait time  $t_{f2}$  (FIG. 9, S4).

It is to be noted that  $t_{y2}$  in the formula above represents the length of time required for the signal electrical charge read, which is uniquely determined by the resolution at the image-capturing element 19 and the

frequency of the drive pulse. In addition, "0.8" in the formula above is a coefficient set to assure the safety of the operation timing and is a value slightly smaller than 1.

- 5 (4) In conformance to the wait time  $t_{f2}$  thus determined, the timing with which the rotation of the sequence motor 25 is started for the next frame is determined (FIG. 9, S15).

Thus, in the fourth embodiment, the operation  
10 timing is automatically adjusted based upon the time count performed in the photographing preparations. Consequently, even when the operation timing gradually changes due to fluctuations in the battery voltage and the like, problems such as the subject image in the next  
15 frame becoming projected onto the photosensitive surface during the electrical charge read period do not occur.

In addition, since it is not necessary to set the wait time  $t_{f2}$  with a margin in correspondence to the fluctuations of the operation timing, the frame speed  
20 can be increased to the maximum degree.

It is to be noted that while shutter charge, mirror down, mirror up and aperture control are started during the electrical charge read period as preparations for photographing in the embodiments explained above, the  
25 present invention is not limited to this example.

For instance, as illustrated in FIG. 10,  
photographing preparations such as the focal point  
detection operation and the photometric operation may be  
started concurrently during the electrical charge read  
5 period.

It is to be noted that in this example, the focal  
point detection operation and the photometric operation  
are executed while the mirror is down, since the camera  
structure in which the subject light is guided to the  
10 focal point detection unit and the photometric unit  
while the mirror is down, as in the electronic camera 11  
in FIG. 1, is the prerequisite in the example. However,  
in an electronic camera that is free of such limitation,  
the focal point detection operation and photometric  
15 operation may be started regardless of the state of the  
mirror to achieve an even higher frame speed.

In addition, while an operation that is performed  
when the continuous shooting mode is set has been  
explained in reference to the embodiments above, the  
20 present invention is not limited to this example. For  
instance, the photographing preparations for the next  
frame may be started during an electrical charge read  
period in a single-shot mode. Through such an operation,  
it becomes possible to reduce the length of time  
25 required to elapse before the next frame can be



photographed even in the single-shot mode. It is to be noted that in this case, the length of the time interval elapsing before the shutter release for the next frame may be measured so that the photographing preparations for the next frame can be performed again if this time interval exceeds a specific length of time. By implementing such an operation, it becomes possible to achieve an optimal balance between higher frame speed and correct photographing preparations for the next frame in the single-shot mode.

The explanation has been given in reference to the embodiments on single lens reflex type electronic camera such as the electronic camera 11 illustrated in FIG. 1 having a quick-return mirror, a shutter mechanism and the like. However, the details of the electronic camera do not need to be limited to these particulars. The particulars of the present invention may be adopted in an electronic camera that is not provided with a quick-return mirror, a shutter mechanism and the like. In other words, the present invention may be adopted in the entire range of electronic cameras that require specific photographing preparations to enable photographing of the next frame.

What is claimed is:

1. An electronic camera comprising:

an image-capturing device that stores a signal  
electrical charge achieved by performing photoelectric  
conversion on a subject image projected on a  
photosensitive surface and reads out the signal  
electrical charge to generate an image signal; and

a photographing preparation device that executes  
photographing preparations for photoelectric conversion  
performed at said image-capturing device, wherein

said photographic preparation device executes at  
least some of said photographing preparations for  
photographing a next frame during an electrical charge  
read period at said image-capturing device.

2. An electronic camera according to claim 1, further  
comprising:

at least one of a shutter mechanism, a mirror  
mechanism, an aperture mechanism, a focal adjustment  
device and a photometric device, wherein

said photographing preparations for the next frame  
include at least one of a shutter charge performed by  
said shutter mechanism, a mirror down performed by said  
mirror mechanism, a mirror up performed by said mirror  
mechanism, aperture control performed by said aperture

mechanism, focal adjustment performed by said focal adjustment device and photometry performed by said photometric device, to enable photographing of the next frame.

5

3. An electronic camera according to claim 1, further comprising:

a shutter mechanism, a mirror mechanism and an aperture mechanism, wherein

10 said photographing preparation device completes a shutter charge performed by said shutter mechanism and a mirror down performed by said mirror mechanism to enable photographing of the next frame and starts a mirror up performed by said mirror mechanism and aperture control  
15 performed by said aperture mechanism to enable photographing of the next frame during an electrical charge read period at said image-capturing device.

4. An electronic camera according to claim 1, further  
20 comprising:

a mirror mechanism, a focal adjustment device and a photometric device, wherein

said photographing preparation device completes a mirror down performed by said mirror mechanism to enable  
25 photographing of the next frame and then implements

focal adjustment by said focal adjustment device and photometry by said photometric device, during an electrical charge read period at said image-capturing device.

5

5. An electronic camera according to claim 1, further comprising:

a continuous shooting command device that issues a command to perform continuous shooting, wherein

10

said photographic preparation device executes at least some of the photographing preparations for the next frame during the electrical charge read period at said image-capturing device while a command to perform continuous shooting issued by said continuous shooting command device is in effect.

15

6. An electronic camera according to claim 1, wherein

said photographing preparation device includes a drive motor that drives the photographing preparation and implements rotational drive of said drive motor during the electrical charge read period at said image-capturing device.

20

7. An electronic camera according to claim 1, wherein

25

said photographing preparation device includes a

drive motor that sequentially drives a plurality of  
photographing preparations in correspondence to a  
rotating angle and implements rotational drive of said  
drive motor during the electrical charge read period at  
5 said image-capturing device.

8. An electronic camera according to claim 1, wherein  
operation timing is set in advance at least at  
either said photographing preparation device or said  
10 image-capturing device to ensure that the electrical  
charge read period does not overlap a period over which  
a subject image of the next frame is projected onto said  
photosensitive surface.

15 9. An electronic camera according to claim 1, wherein  
said photographing preparation device performs  
detection of a completion of a signal electrical charge  
read operation performed by said image-capturing device  
and following the detection, projects a subject image  
20 onto said photosensitive surface.

10. An electronic camera according to claim 1, further  
comprising:  
a time count device that measures at least a part  
25 of a length of time required for the photographing

preparation, wherein

at least either said photographing preparation  
device or said image-capturing device adjusts operation  
timing to ensure that a signal electrical charge read  
5 period does not overlap a period over which a subject  
image for the next frame is projected onto said  
photosensitive surface based upon results of count of  
the length of required time performed by said time count  
device.

ABSTRACT OF THE DISCLOSURE

An electronic camera includes: an image-capturing device that stores a signal electrical charge achieved  
5 by performing photoelectric conversion on a subject image projected on a photosensitive surface and reads out the signal electrical charge to generate an image signal; and a photographing preparation device that executes photographing preparations for photoelectric  
10 conversion performed at the image-capturing device. And the photographic preparation device executes at least some of the photographing preparations for photographing a next frame during an electrical charge read period at the image-capturing device.

FIG. 1

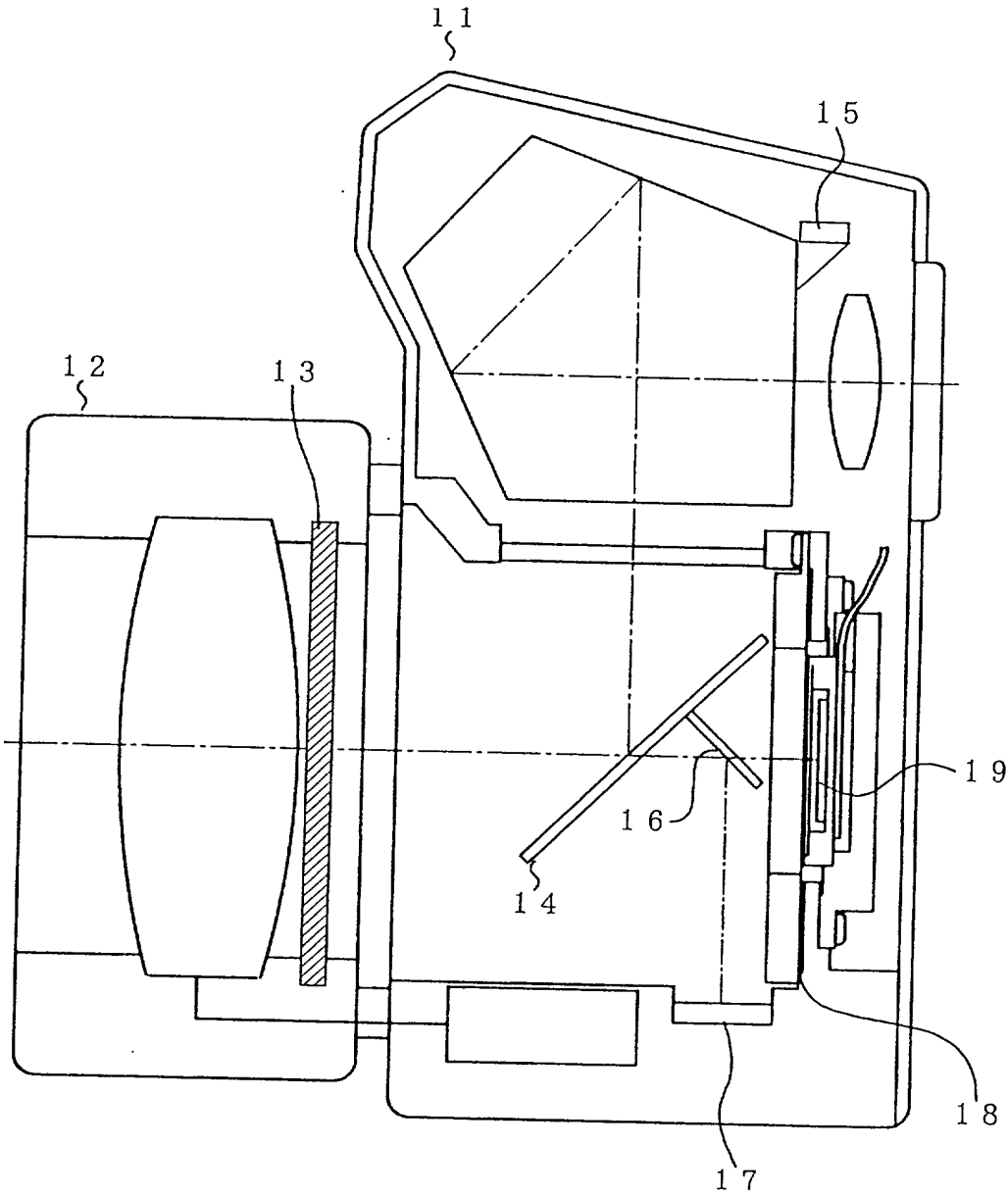




FIG. 2

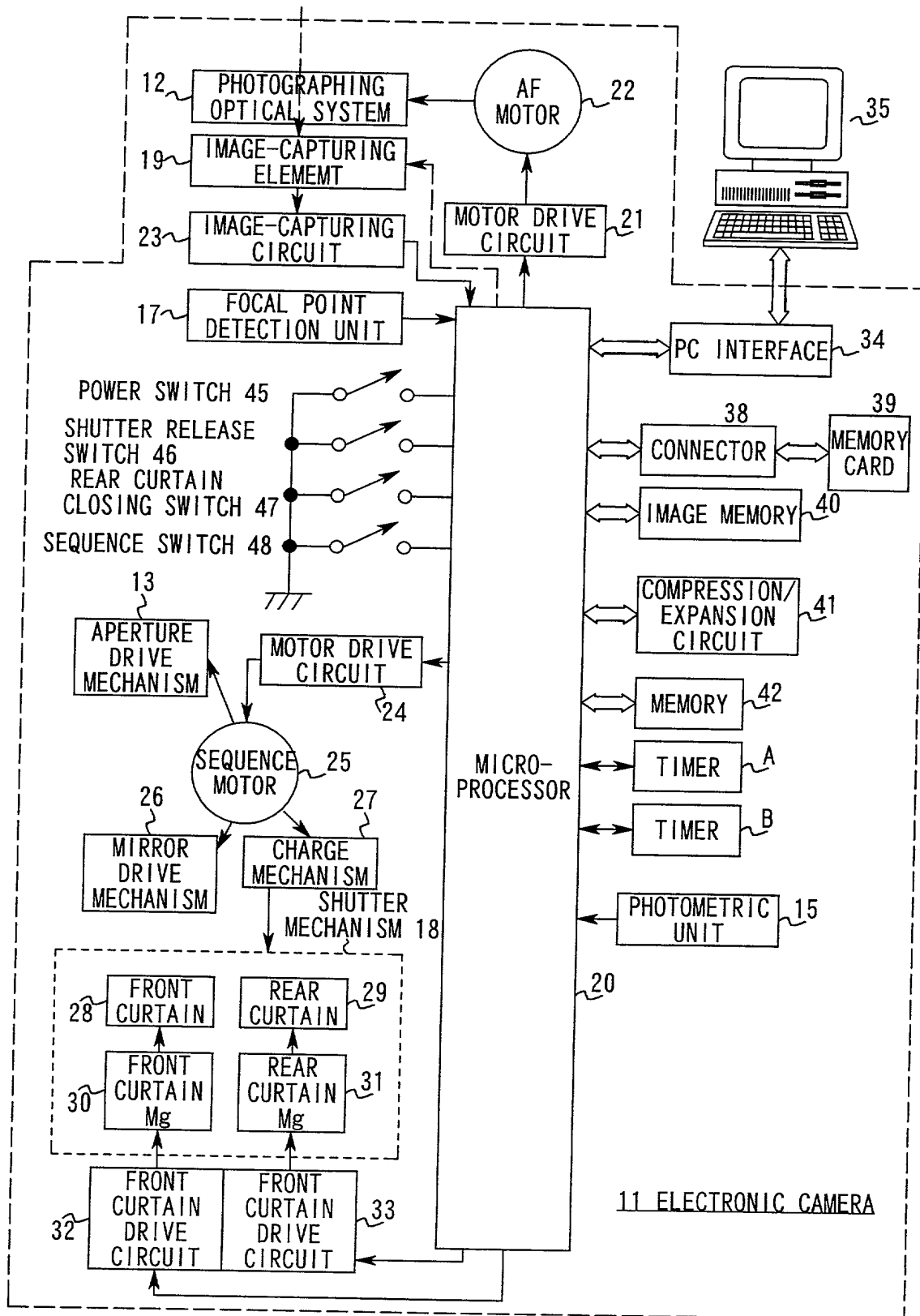


FIG. 3

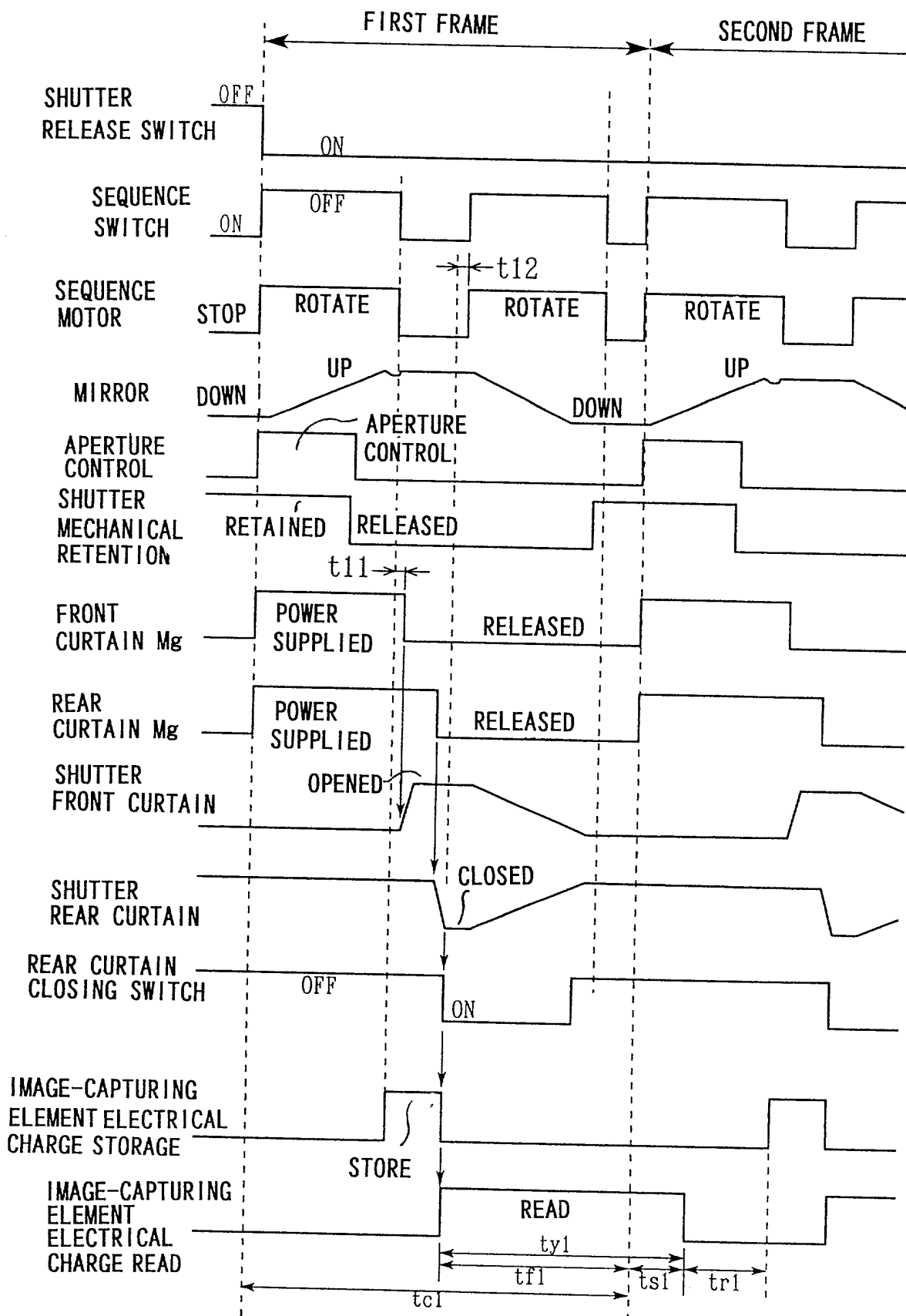


FIG. 4

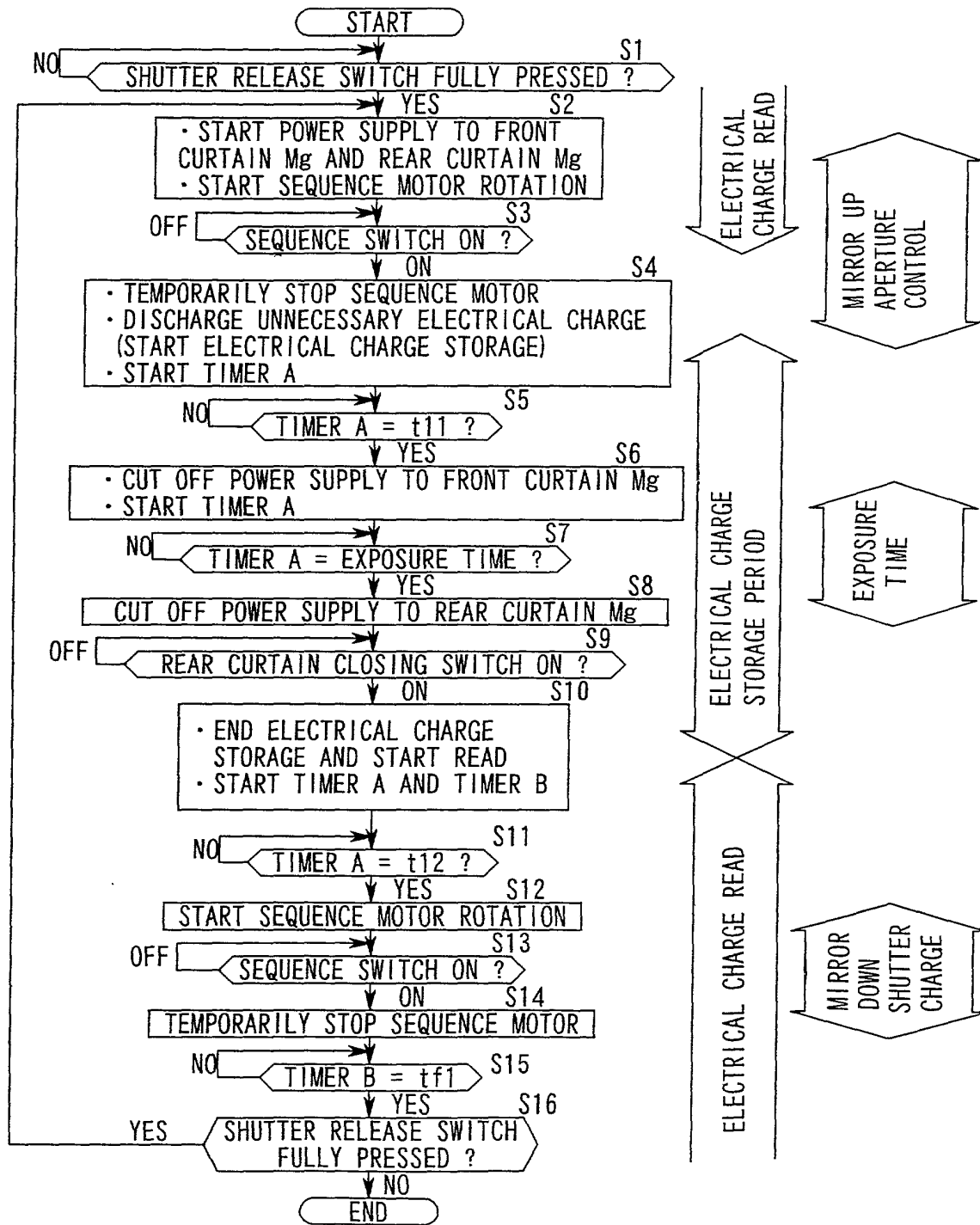


FIG. 5

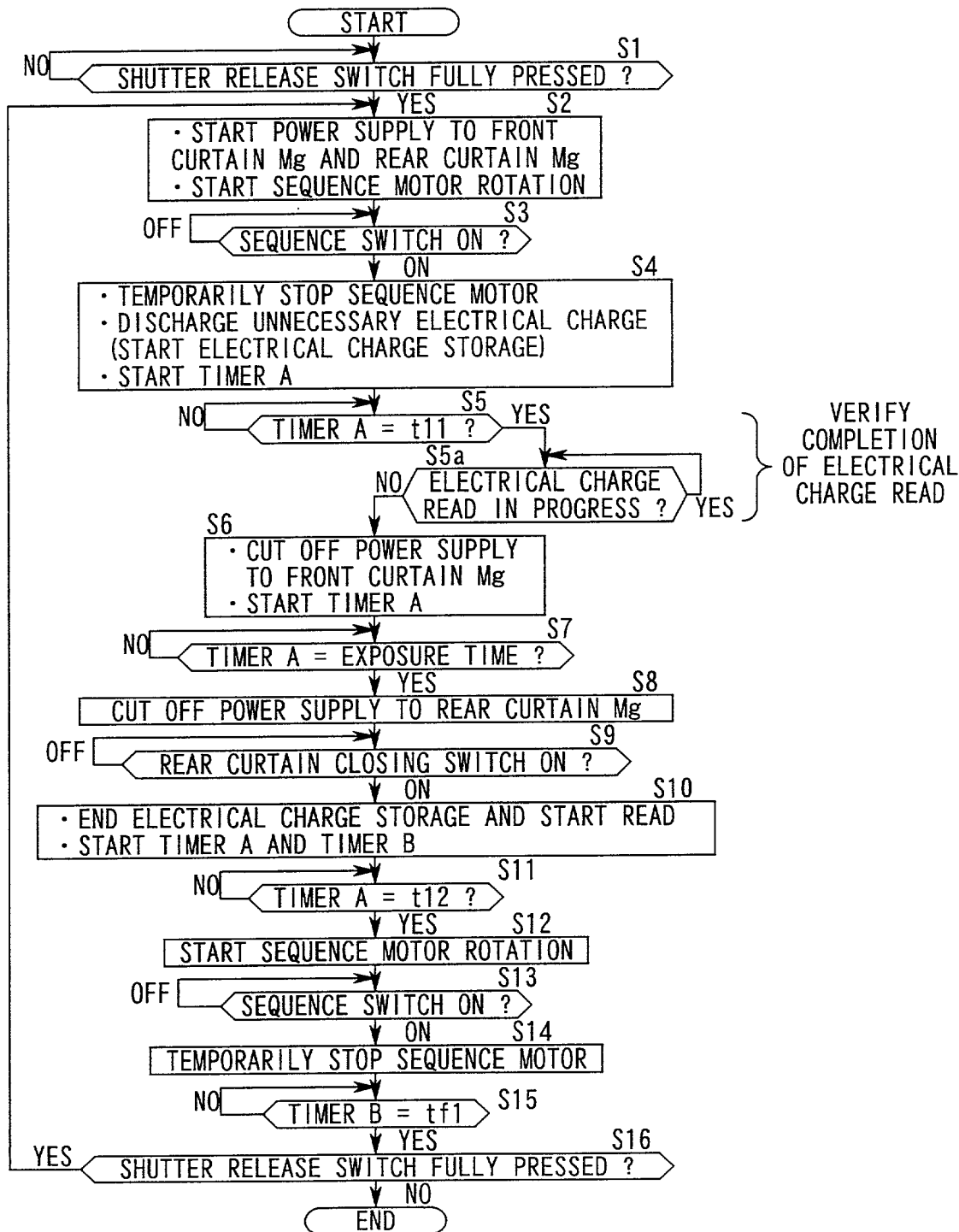


FIG. 6

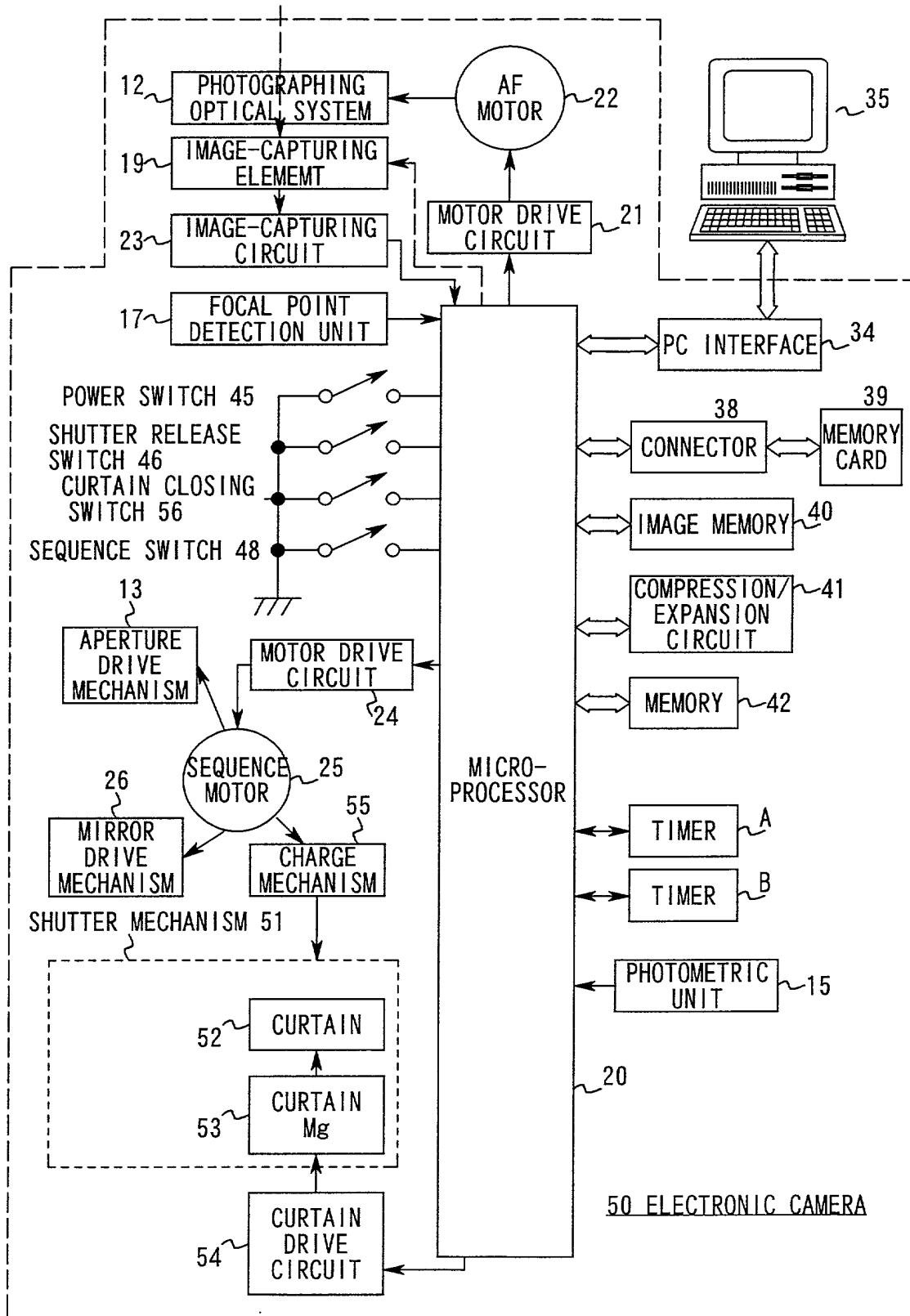
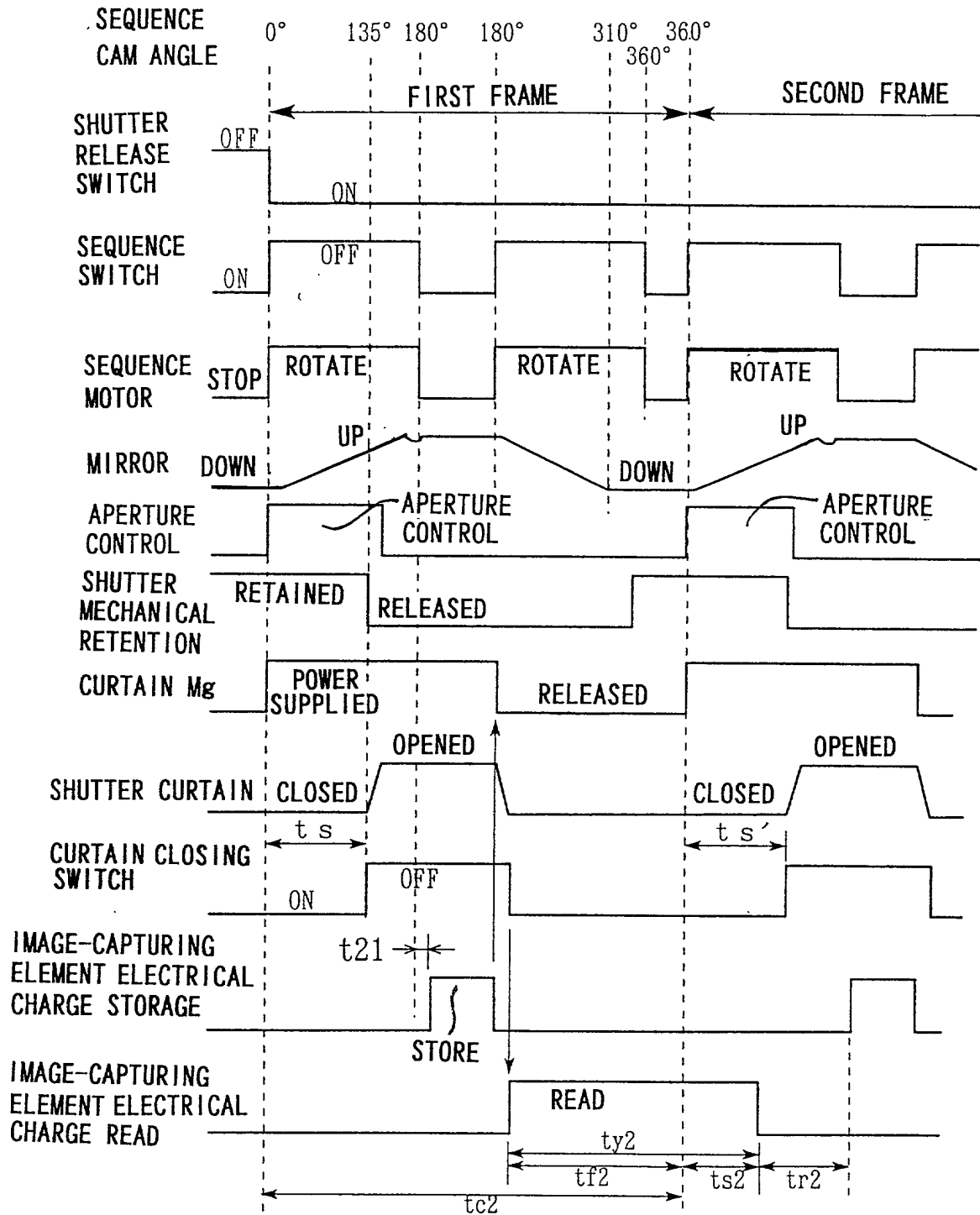


FIG. 7



00001310 072759

FIG. 8

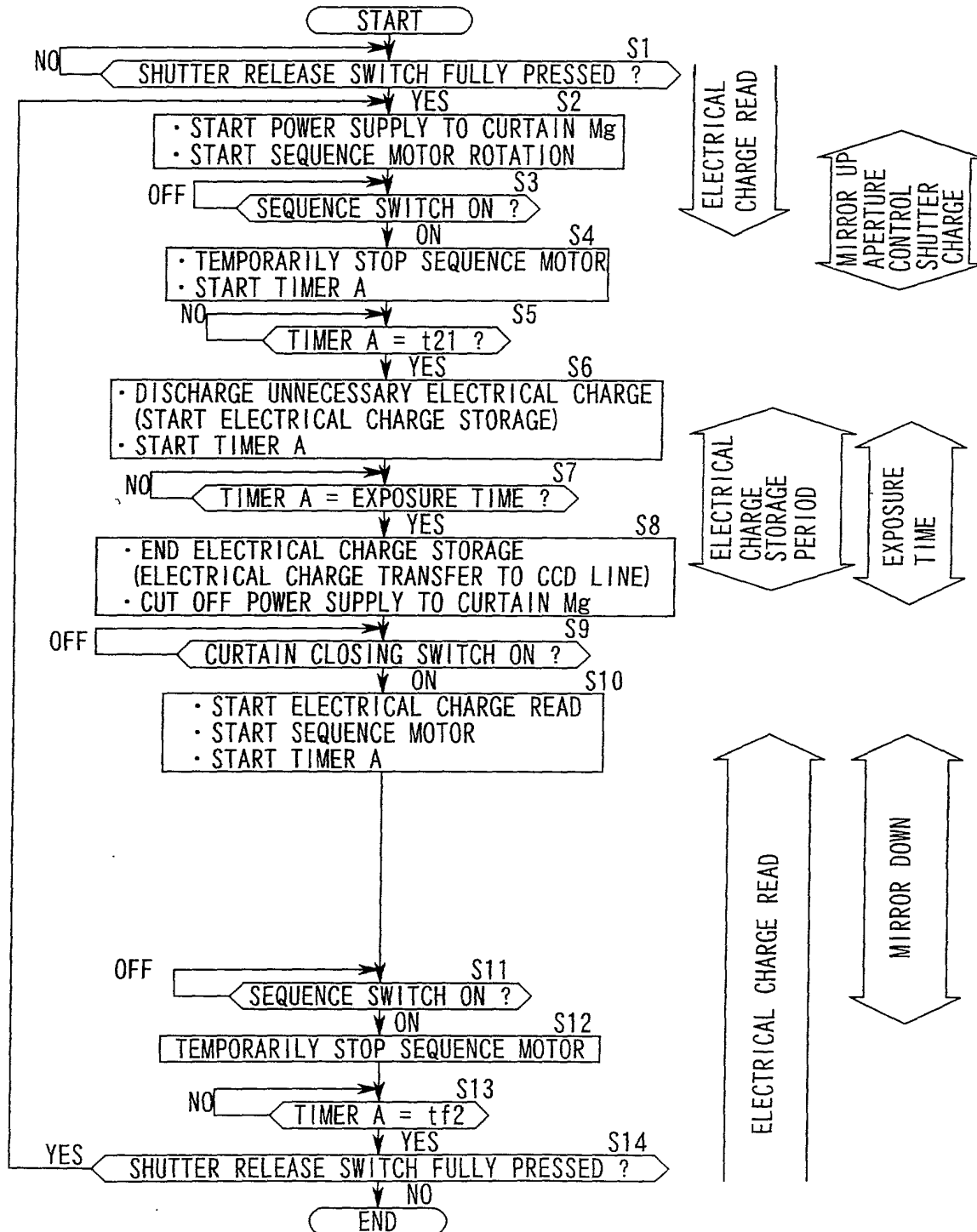
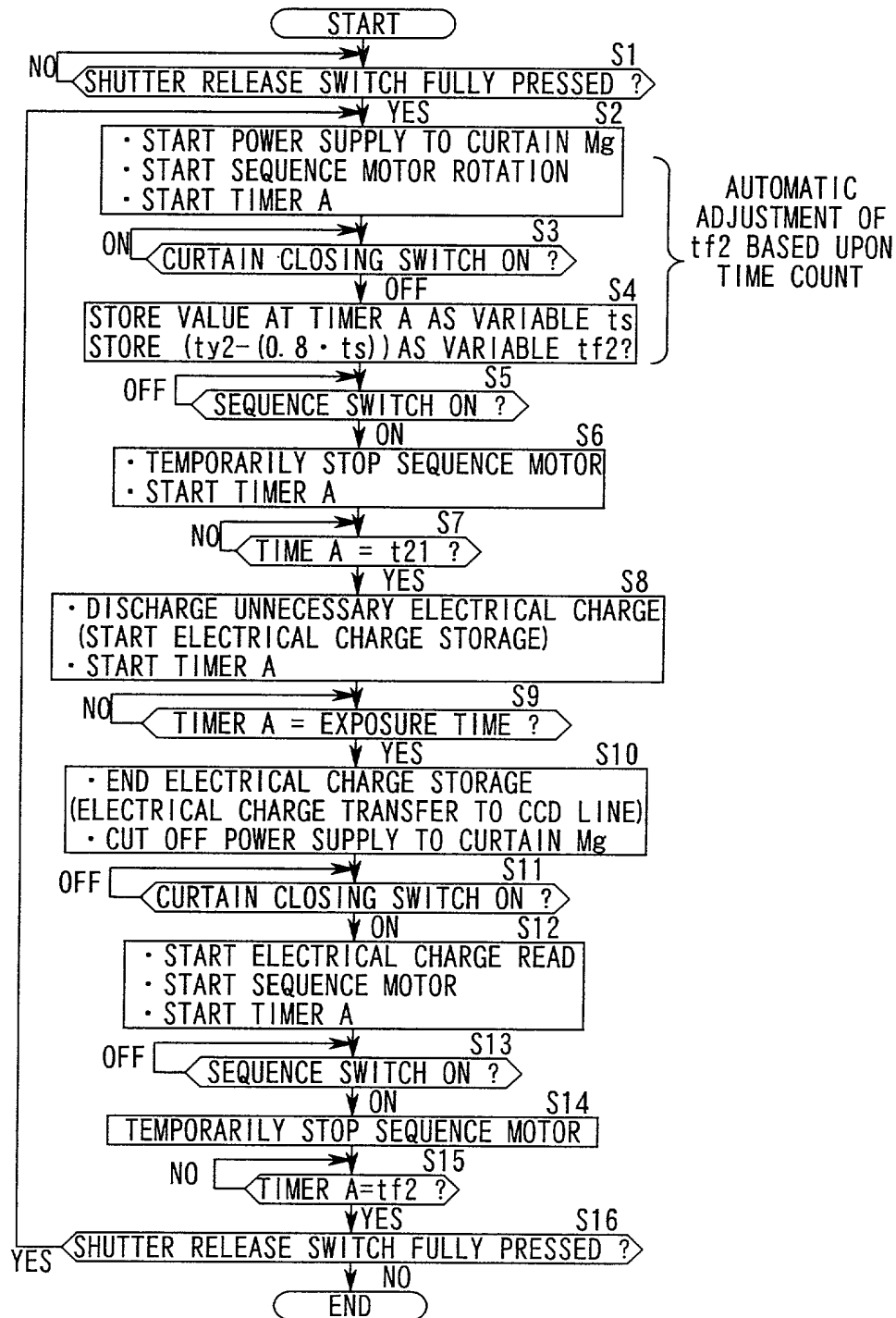


FIG. 9





# FIG. 10

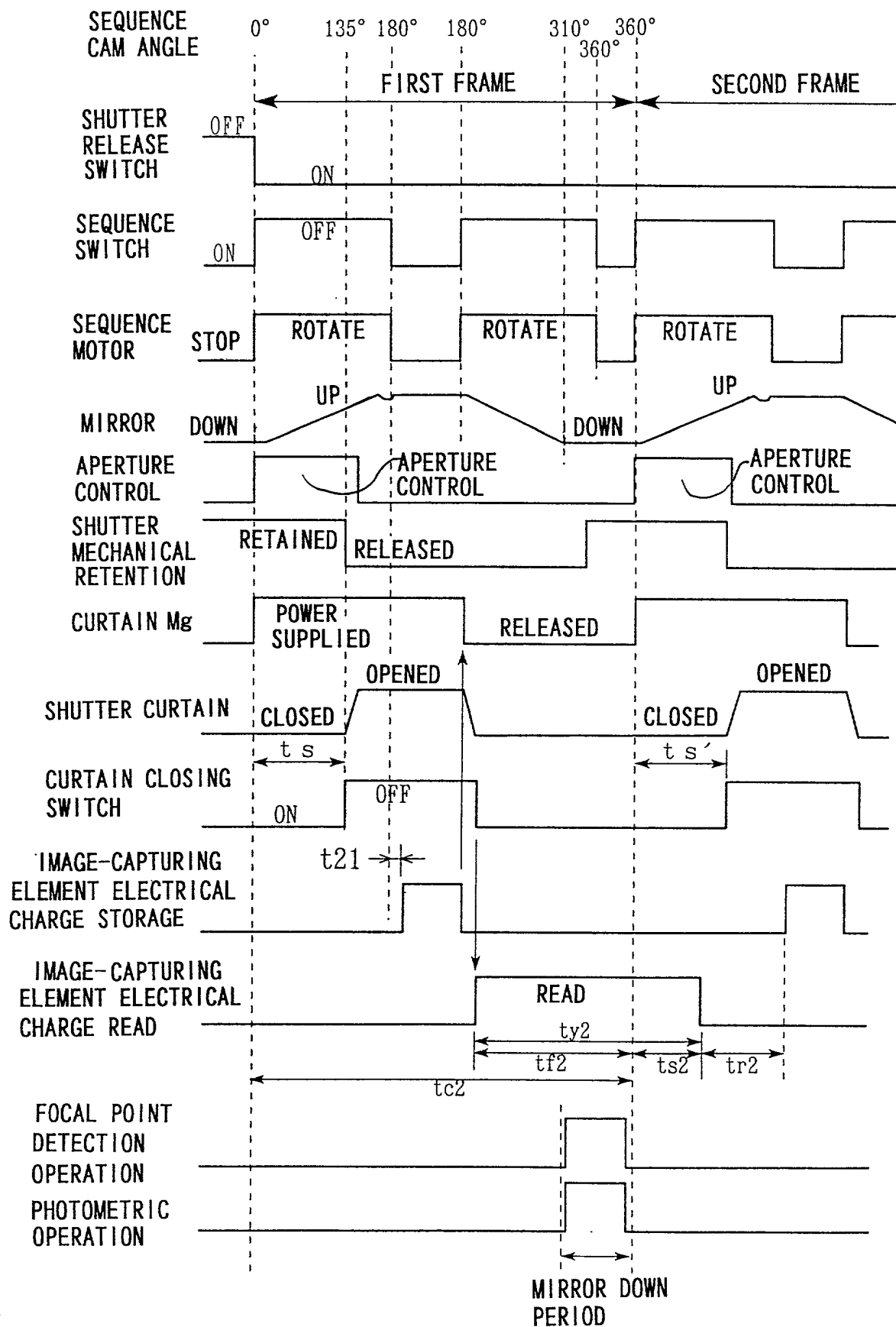
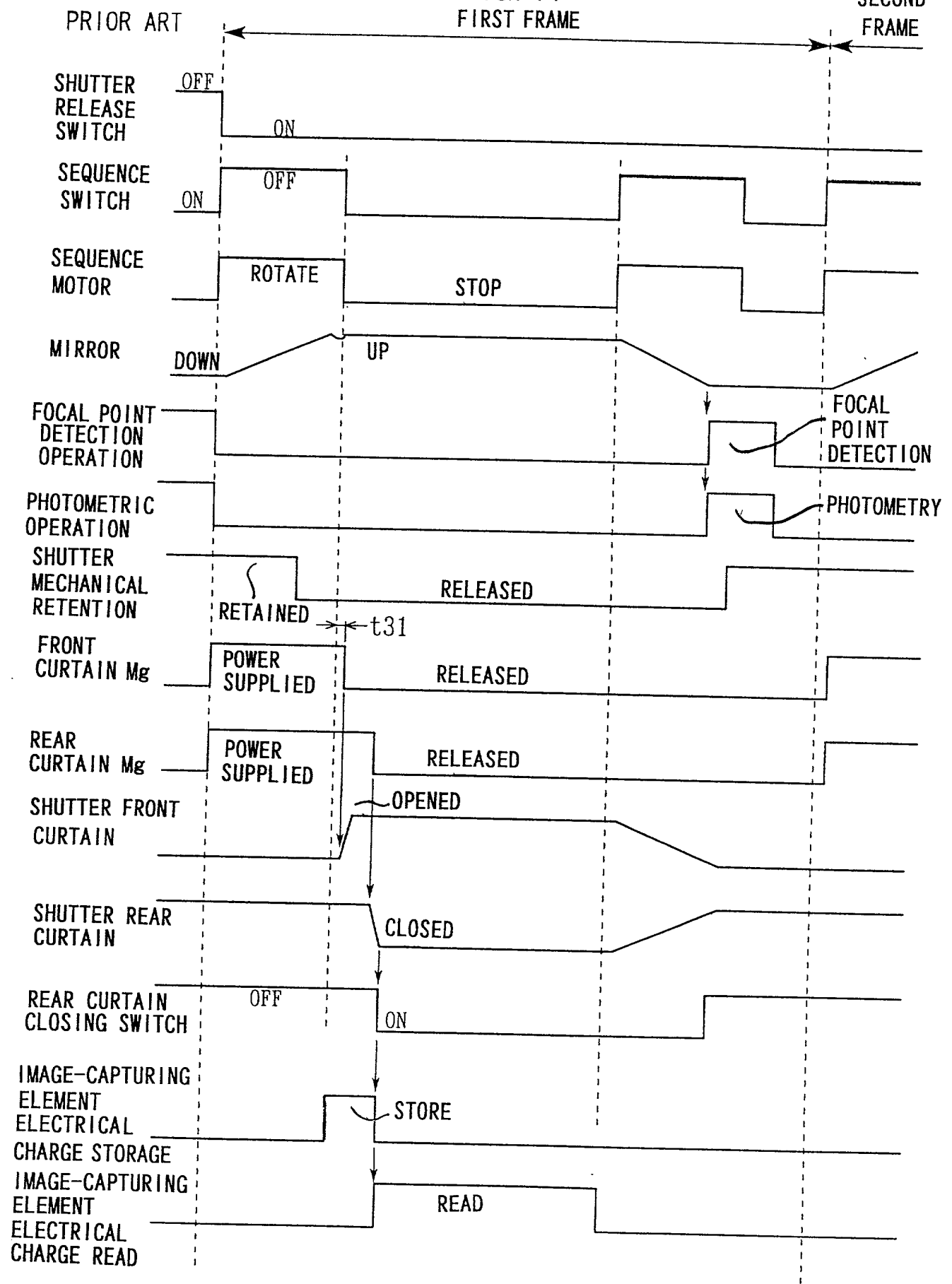


FIG. 11  
FIRST FRAME

SECOND  
FRAME



# APPLICATION FOR UNITED STATES PATENT DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name; that

I verily believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: ELECTRONIC CAMERA

described and claimed in the specification:

**Check one**

\*a. ☒ attached hereto.

b. ☐ filed on \_\_\_\_\_ as Application No. \_\_\_\_\_ and amended on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, §1.56.

Under Title 35, U.S. Code §119, the priority benefits of the following foreign application(s) and/or United States provisional application(s) filed within one year prior to this application are hereby claimed:

Japanese Patent Application No.10-213207 filed July 28,1998

The following application(s) for patent or inventor's certificate on this invention were filed in countries foreign to the United States of America either (a) more than one year prior to this application, or (b) before the filing date of the above-named foreign priority application(s) and/or United States provisional application(s):

I hereby appoint the following as my attorneys of record with full power of substitution and revocation to prosecute this application and to transact all business in the Patent Office:

**James A. Oliff, Reg. No. 27,075; William P. Berridge, Reg. No. 30,024;  
Kirk M. Hudson, Reg. No. 27,562; Thomas J. Pardini, Reg. No. 30,411;  
Edward P. Walker, Reg. No. 31,450; Robert A. Miller, Registration No. 32,771 and  
Mario A. Costantino, Registration No. 33,565.**

**ALL CORRESPONDENCE IN CONNECTION WITH THIS APPLICATION SHOULD BE SENT TO OLIFF & BERRIDGE, P.L.C., P.L.C., P.O. BOX 19928, ALEXANDRIA, VIRGINIA 22320, TELEPHONE (703) 836-6400.**

I hereby declare that I have reviewed and understand the contents of this Declaration, and that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

1

**Typewritten Full Name  
of First or Sole Inventor**

Akira EZAWA

Given Name Middle Initial Family Name

2

**\*\*Inventor's Signature:**

Akira

Ezawa

3

**\*\*Date of Signature:**

July

12

1999

Month

Day

Year

**Residence:**

Kawasaki-shi

KANAGAWA

JAPAN

City

State or Province

Country

**Citizenship:**

Japan

**Post Office Address:** C/O Nikon Corporation, Fuji Bldg., 2-3, Marunouchi  
(Insert complete  
mailing address, 3-chome, Chiyoda-ku, TOKYO 100-8331 JAPAN  
including country)

\*If Box (a.) is checked, this form may be executed only when attached to the specification (including claims).

\*\*Note to Inventor: Please sign name exactly as it appears above and insert actual date of signing.

**IF THERE IS MORE THAN ONE INVENTOR USE PAGE 2 AND PLACE AN "X" HERE** ☐